

Status and Progress of Schottky Technology Development for SWI and ISMAR

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Abstract— We present the progress of the technological development of a full e-beam based monolithically integrated Schottky diode process applicable for sub-millimetre wave multipliers and mixers. The process has been employed in a number of demonstrators showing state-of-the-art performance.

I. INTRODUCTION

There is a need for developing efficient and reliable heterodyne receivers operating in the sub-millimetre wave band above 500 GHz for future science missions and Earth observation instruments such as International SubMillimetre Airborne Radiometer (ISMAR) and the Submillimetre Wave Instrument (SWI)[1,2]. The sub-millimetre wave regime allows studying different meteorological phenomena such as water vapour, cloud ice water content, ice particle sizes and distribution, which are important parameters for the hydrological cycle of the climate system and the energy budget of the atmosphere. Similar data can be collected from the atmospheres of planets and moons.

ISMAR is an airborne platform dedicated for the demonstration and validation of microwave atmospheric sounding instruments in 200 GHz to 1000 GHz range and currently under development by the Met Office in UK.

SWI is a part of JUPITER ICy moons Explorer (JUICE) mission, which is the next large European science mission planned for launch in 2022 and arrival at Jupiter in 2030. This mission will tour the giant planet to explore its atmosphere, magnetosphere and tenuous set of rings and will characterise the icy moons Ganymede, Europa and Callisto mapping their surfaces, sounding their interiors and assessing their potential for hosting life, as the moons are thought to harbor vast amounts of water.

The operational frequency bands for science missions carrying planetary atmosphere characterisation instruments are moving towards up to several Terahertz (THz) [3,4]. While discrete GaAs Schottky diodes and hybrid circuits are adequate for frequencies up to 400 GHz, monolithic integration is necessary at higher frequencies due to better fabrication and alignment tolerances as well as to enable more advanced circuit integration. Moreover, the performance and

functionality of discrete diode circuit designs, is limited by the shape and thickness of the supporting substrate. Monolithic integrated circuits (MICs), supported by a thin membrane, overcome restrictions imposed by the substrate thickness [5-7]. On the other hand, a drawback of GaAs Schottky based sub-systems is that they traditionally require relatively high local oscillator (LO) powers, in the order of \sim mW, compared to SIS and HEB technologies, which operate at \sim μ W levels. The LO power requirements have to be considered when moving to higher frequencies \sim 1 THz. For applications with limited mass and power budget, applying biasing schemes or switching to low bandgap material systems e.g. InGaAs, becomes an interesting solution, as the required LO power can be reduced up to an order in magnitude. These options however come with the downside of increased mixer noise.

In the presentation we would like to show a current status of process development of THz MIC GaAs Schottky membrane mixers and multipliers and an alternative Schottky diode mixers based on InGaAs material.

II. DIODE FABRICATION

One of the process requirements for mixers operating at THz frequencies is anode's area, which becomes as small as $0.15 \mu\text{m}^2$ or even smaller (Fig.1). The Chalmers diode process is based on electron beam lithography, with a beam spot of less than 5 nm, allowing precise and repeatable anode and air bridge formation.

A. GaAs Schottky diode mixers and multipliers on membrane

For the diodes on membrane, the starting structure is a semi-insulating GaAs substrate supporting a $3 \mu\text{m}$ thick GaAs layer sandwiched in between two AlGaAs etch stop layers and a buffer and an active layers. The Chalmers THz MIC membrane Schottky process is described in [8,9]. For process of THz MIC a resist reflow is used to form a stream-line slope across the mesas followed by a metal deposition and lift-off. Scanning Electron Microscope (SEM) image of an integrated mixer diode is shown in Fig.2.

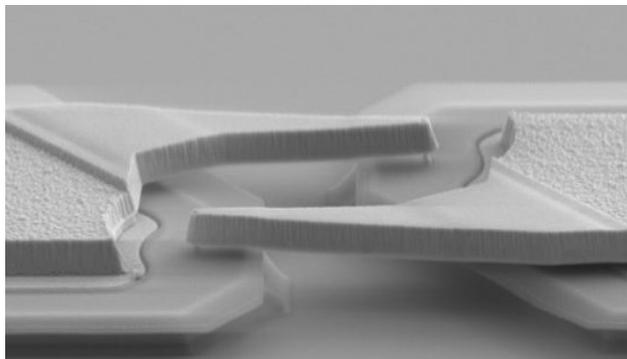


Figure. 1 SEM image of an antiparallel diode with an anode area of $0.1 \mu\text{m}^2$ designed for operating at 1.2 THz and fabricated at MC2 Chalmers.

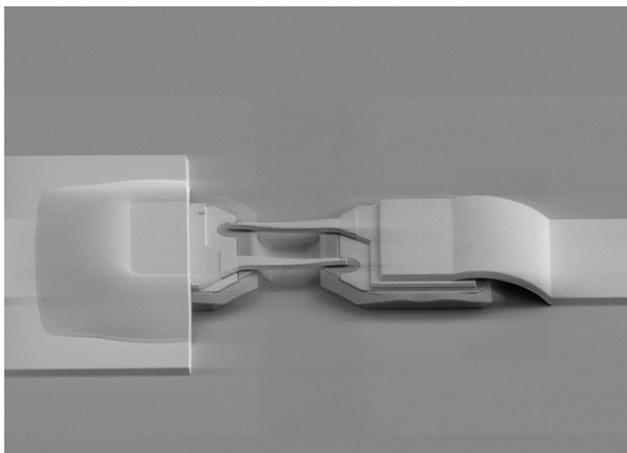


Figure. 2 SEM image of the monolithic integrated antiparallel diodes fabricated at the MC2 process laboratory at Chalmers.

B. InGaAs Schottky diode mixers

Process for InGaAs Schottky diode is quite similar to that for GaAs Schottky diode, except for that the mesa etching and e-beam dose are carefully optimized for the different materials.

III. CONCLUSIONS

A full e-beam integrated Schottky diode membrane process dedicated for THz operation, has been developed at Chalmers University of Technology. The process is currently supporting

several THz science projects, e.g. the development of 874 GHz front-end receivers for ISMAR instrument, and SWI 600/1200 GHz broadband spectrometer front-ends for JUICE mission, as well as the development of THz harmonic mixers for QCL phase locking applications up to 4.7 THz [10].

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